Rectifier Diode



DS5995 - 1 March 2011 (LN18181)

FEATURES

- Double Side Cooling
- High Surge Capability

VOLTAGE RATINGS

Part and Ordering Number	Repetitive Peak Voltages V _{RRM} V	Conditions
DRD4650C14 DRD4650C12 DRD4650C10 DRD4650C08 DRD4650C06	1400 1200 1000 800 600	$V_{RSM} = V_{RRM} + 100V$

ORDERING INFORMATION

When ordering, select the required part number shown in the Voltage Ratings selection table.

For example:

DRD4650C14 for a 1400V device

KEY PARAMETERS

V_{RRM}	1400V
$I_{F(AV)}$	4650A
I _{FSM}	45000A

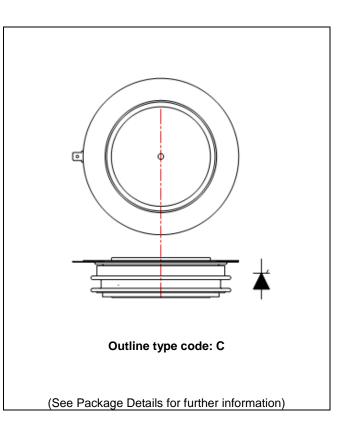


Fig. 1 Package outline



CURRENT RATINGS

$T_{case} = 75$ °C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units			
Double Si	Double Side Cooled						
I _{F(AV)}	Mean forward current	Half wave resistive load	5510	А			
I _{F(RMS)}	RMS value	-	8650	Α			
I _F	Continuous (direct) on-state current	-	7790	А			

T_{case} = 100°C unless stated otherwise

Symbol	Parameter	Test Conditions		Units			
Double Si	Double Side Cooled						
I _{F(AV)}	Mean forward current	Half wave resistive load	4650	Α			
I _{F(RMS)}	RMS value	-	7300	Α			
I _F	Continuous (direct) on-state current	-	6580	А			

SURGE RATINGS

Symbol	Parameter	Test Conditions	Max.	Units
I _{FSM}	Surge (non-repetitive) on-state current	10ms half sine, T _{case} = 190°C	45.0	kA
l ² t	I ² t for fusing	$V_R = 0$	10.13	MA ² s



THERMAL AND MECHANICAL RATINGS

Symbol	Parameter	Test Conditions	6	Min.	Max.	Units
R _{th(j-c)}	Thermal resistance – junction to case	Double side cooled	DC	-	0.0125	°C/W
R _{th(c-h)}	Thermal resistance – case to heatsink	Double side cooled	DC	-	0.004	°C/W
T _{vj}	Virtual junction temperature	Blocking V _{DRM} / _{VRRM}		-40	190	°C
T _{stg}	Storage temperature range			-40	190	°C
F _m	Clamping force			40	50	kN

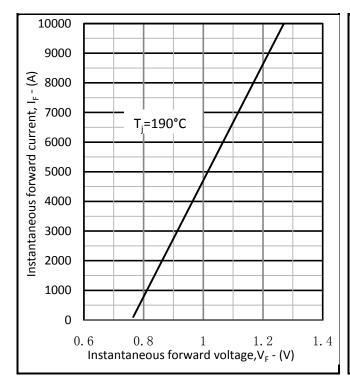
CHARACTERISTICS

Symbol	Parameter	Test Conditions	Min.	Max.	Units
V _{FM}	Forward voltage	At 3000A peak, T _{case} = 25°C	-	1.05	V
I _{RM}	Peak reverse current	At V _{DRM} , T _{case} = 190°C	-	250	mA
0	Total stored charge	$I_F = 4000A$, $dI_{RR}/dt = 10A/\mu s$	_	4500	μC
Q _S	Total Stored Charge	T _{case} = 190°C, V _R =100V	-		μΟ
V_{TO}	Threshold voltage	At T _{vj} = 190°C	-	0.76	V
r _T	Slope resistance	At T _{vj} = 190°C	-	0.051	mΩ

3/6



CURVES



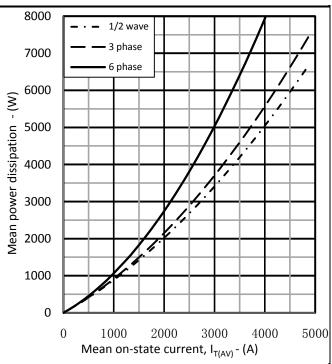
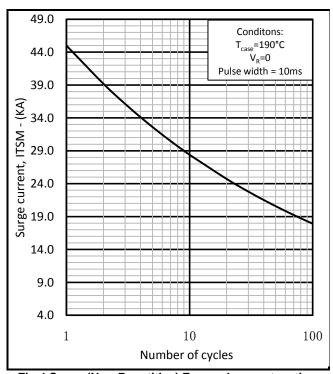


Fig.2 Maximum forward characteristics







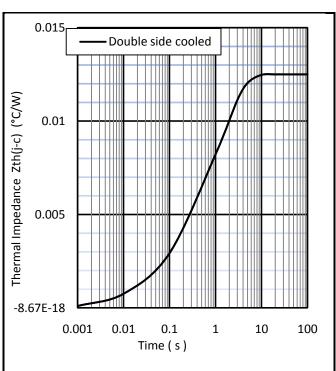
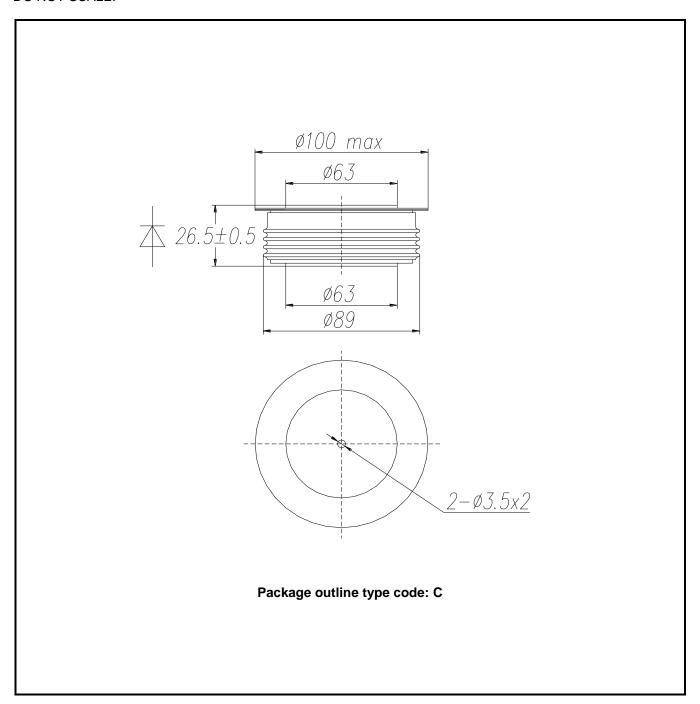


Fig.5 Maximum (limit) transient thermal impedancejunction to case



PACKAGE DETAILS

For further package information, please contact Customer Services. All dimensions in mm, unless stated otherwise. DO NOT SCALE.



Note:

Some packages may be supplied with gate and or tags.

5/6



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No actual design work on the product has been started.

Preliminary Information: The product design is complete and final characterisation for volume production is in

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